

JC670 U.S. PRO
09/150596

	Subclass	ISSUE CLASSIFICATION
	Class	

Best Available Copy

U.S. UTILITY Patent Application

JCS
TC834

1140 O.I.P.E.
SCANNED 7/13 Q.A. AG

PATENT DATE

APPLICATION NO. 09/750596	CONT/PRIOR D	CLASS 709	SUBCLASS 225.	ART UNIT [REDACTED]	EXAMINER Sheikh
------------------------------	-----------------	--------------	------------------	------------------------	--------------------

WTS Jack Lerner

2157

Method and apparatus for integrating circuit boards into a system

6

PTO-2040
12/99

ISSUING CLASSIFICATION

<input type="checkbox"/> TERMINAL DISCLAIMER		DRAWINGS			CLAIMS ALLOWED	
		Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.		(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____		(Primary Examiner) _____ (Date) _____			ISSUE FEE	
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.		(Legal Instruments Examiner) _____ (Date) _____			ISSUE BATCH NUMBER	

WARNING:

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-438A
(Rev. 6/99)

FILED WITH: DISK (CRF) FICHE CD-ROM

(Attached in pocket on right inside flap)